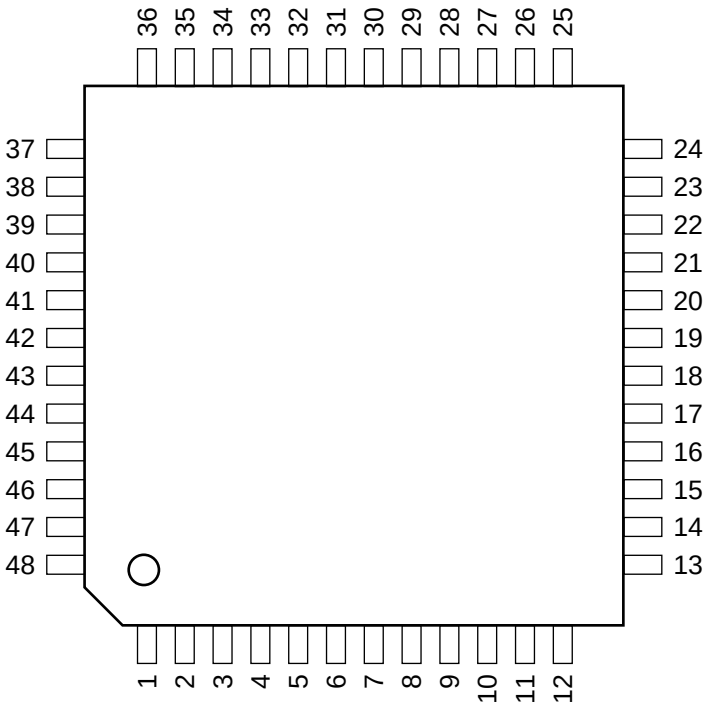
VIDEO A/D CONVERTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	D0	13	—	NC	25	—	AV_{DD}	37	—	NC
2	O	D1	14	—	NC	26	—	AV_{DD}	38	O	CM
3	O	D2	15	—	NC	27	—	AGND	39	I	VIN
4	O	D3	16	—	DGND	28	—	AGND	40	—	NC
5	O	D4	17	—	NC	29	I	LPBY	41	—	NC
6	—	NC	18	—	NC	30	I	REFT	42	—	NC
7	—	NC	19	—	NC	31	—	NC	43	—	AGND
8	O	D5	20	—	NC	32	—	NC	44	—	AGND
9	O	D6	21	—	NC	33	—	NC	45	—	DV_{DD}
10	O	D7	22	—	CLK	34	I	REFB	46	—	NC
11	O	D8	23	I	$\overline{OE}$	35	I	LNBY	47	—	NC
12	O	D9	24	I	$\overline{CE}$	36	—	AGND	48	—	DGND

INPUTS

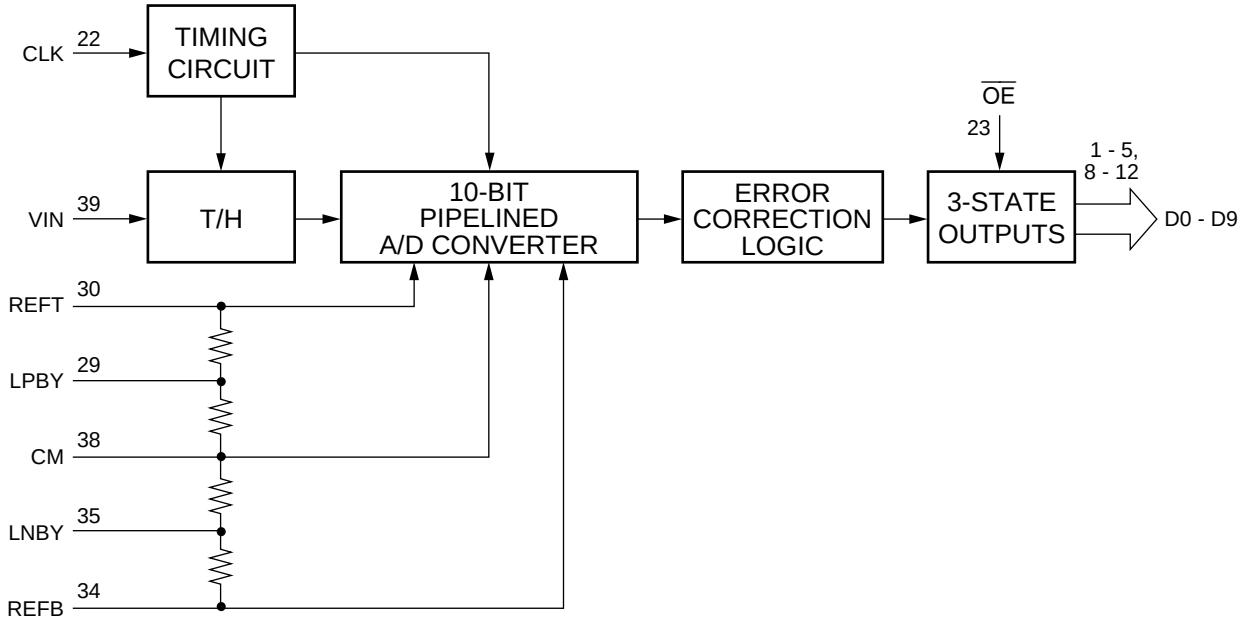
$\overline{CE}$: CHIP ENABLE
CLK : CONVERT CLOCK
$\overline{OE}$: OUTPUT ENABLE
LPBY : POSITIVE LADDER BYPASS
LNBY : NEGATIVE LADDER BYPASS
REFB : BOTTOM REFERENCE VOLTAGE
REFT : TOP REFERENCE VOLTAGE
VIN : ANALOG IN

OUTPUTS

CM : COMMON MODE VOLTAGE
D0 - D9 : DATA BIT

OTHER

NC : NO CONNECTION


CLK
22
TIMING CIRCUIT
VIN
39
T/H
10-BIT PIPELINED A/D CONVERTER
ERROR CORRECTION LOGIC
OE
23
3-STATE OUTPUTS
1 - 5, 8 - 12
D0 - D9
REFT
30
LPBY
29
CM
38
LNBY
35
REFB
34